

**Amendments to the Specification:**

Please replace paragraph [0018] with the following amended paragraph:

[0018]

**DESCRIPTION OF THE ~~PREFERRED~~ PREFERRED EMBODIMENT**

FIG. 1 shows a first embodiment of the electric power supply unit according to the present invention.

That is, in FIG. 1, regulator 2 (~~a first regulator~~) is connected to battery 1, and battery voltage V1 supplied by battery 1 is supplied to regulator 2. This regulator 2 converts battery voltage V1 of 22V for instance into a fixed voltage (for instance, 7.8V) and outputs it. Regulator 3 (~~a third regulator~~) and regulator 4 (~~a second regulator~~) are connected to the output terminal of this regulator 2.

Please replace paragraph [0021] with the following amended paragraph:

[0021]

This regulator 3(~~the third regulator~~) generates voltage of 5V which is most suitable for, for example, the I/O power supply of the microcomputer from output voltage V2 (~~the first voltage~~) output from regulator 2 (~~the first regulator~~), and outputs the voltage to microcomputer 8 as output voltage V3 (~~the second voltage~~). Moreover, this regulator 4 (~~the second regulator~~) generates voltage of 3.3V which is most suitable for the CPU core power supply of the microcomputer from output voltage V2 output from regulator 2 (~~the first regulator~~), and outputs the voltage to microcomputer 8 as output voltage V4.

Please replace paragraph [0022] with the following amended paragraph:

[0022]

Regulator 2 (~~the first regulator~~) generates by using battery voltage V1 such a voltage that the loss of regulator 3 (~~the third regulator~~) and regulator 4

~~(the second regulator)~~ can be decreased and the target voltage V3a of regulator 3 and the target voltage V4a of regulator 4 can be output, and outputs it.

Please replace paragraph [0024] with the following amended paragraph:

[0024]

Voltage detector 6 detects the output voltage of regulator 3 ~~(the third regulator)~~. Voltage detector 6 outputs an OFF signal to regulator 4 ~~(the second regulator)~~ when the detected output voltage of regulator 3 drops less than the first set voltage, and stops regulator 4. Further, voltage detector 6 outputs an ON signal to regulator 4 ~~(the second regulator)~~ when the detected output voltage of regulator 3 rises more than the second set voltage, and reactivates regulator 4 which is at rest temporarily.

Please replace paragraph [0027] with the following amended paragraph:

[0027]

~~Although there are provided the first regulator and the second regulator in the configuration according to claim 1, this first regulator corresponds to regulator 2~~ Regulator 3 shown in FIG. 1 ~~[[,]]—which~~ generates voltage of 5V suitable for the I/O power supply unit of the microcomputer from battery voltage V1 supplied by battery 1, and outputs the voltage to microcomputer 8 as output voltage V2 (the first voltage).

Please delete paragraph [0028]:

~~[0028]~~

~~Although there are provided three regulators, the first regulator, the second regulator and third regulator in the configuration according to claim 5 or claim 6, this first regulator corresponds to regulator 2 shown in FIG. 1, the third regulator 3 shown in FIG. 1, and the second regulator 4 shown in FIG. 1. Further, the first voltage detection means recited in claim 5 corresponds to~~

~~voltage detector 6 shown in FIG. 1.~~

Please delete paragraph [0029]:

{0029}

~~Further, the second voltage detection means recited in claim 7 corresponds to voltage detector 5 shown in FIG. 1.~~

Please replace paragraph [0054] with the following amended paragraph:

[0054]

As described above, in controller 20 of regulator 2, the starting/stopping of switching device 21 of regulator 2 (starting/stopping of regulator 2) is decided depending on detection signal ~~D6~~ D5 output from detector ~~6~~ 5 and detection signal D7 output from overheating detector 7.